

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended): A transmitter system for signal conversion, comprising:
 - a spreading code generator that produces a direct sequence spread spectrum (DS-SS) signal and a frequency hopped spread spectrum (FH-SS) signal and combines the DS-SS and FH-SS signals to provide a spreading signal code;
 - a spreader that combines the spreading signal code with an input signal to provide a spread input signal;
 - a signal converter that converts the frequency converted spread input signal from a first domain to a second domain to provide a converted spread input signal; and
a mixer for frequency converting the spread input signal to provide an upconverted spread input signal;
 - a despreader that despreads the converted upconverted spread input signal to provide the input signal in the second domain; and
an antenna that transmits the despread input signal.
- 2-4. (Cancelled).
5. (Original) The system of claim 1, further comprising a feedback loop coupling the despread to the spreader for time aligning the despread with the spreading.
6. (Original) The system of claim 1, wherein the first domain is one of a digital domain and an analog domain and the second domain is the other of the digital domain and the analog domain.
7. (Cancelled)

8. (Original) The system of claim 1, wherein the signal converter is one of a delta-sigma analog-to-digital converter (ADC) and a delta-sigma digital-to-analog converter (DAC).
9. (Original) The system of claim 1, further comprising a clipping component that reduces peaks associated with the spread input signal, the despreader mitigates degradation and out-of-band (OOB) emissions associated with the peak reduction.
10. (Original) The system of claim 1, wherein at least one of the spreader and the despreader circuit comprises a mixer.
11. (Cancelled).
12. (Original) A transmitter comprising the system of claim 1.
13. (Currently Amended) A signal conversion system comprising:
 - a spreading code generator that produces a direct sequence spread spectrum (DS-SS) signal spreading code;
 - a spreading circuit that receives an input signal and combines the input signal with the DS-SS signal spreading code to provide a spread input signal;
 - a clipping component that reduces peaks associated with the spread input signal; and
 - a despreading circuit that despreads the peak reduced spread input signal.
14. (Original) The system of claim 13, wherein at least one of the spreading circuit and despreading circuit comprises a mixer.
15. (Original) The system of claim 13, further comprising a signal converter that converts the spread input signal from a first domain to second domain, the signal converter being one of a digital-to-analog converter (DAC) and an analog-to-digital converter (ADC).

16. (Original) The system of claim 15, the signal converter being one of a delta-sigma DAC and a delta-sigma ADC.

17. (Currently Amended) The system of claim 15, further comprising a second signal converter for converting the spread input signal from the second domain to the first domain.

18. (Original) The system of claim 15, further comprising a mixer for frequency converting the spread input signal one of before signal conversion and after signal conversion.

19. (Currently Amended): A method for signal conversion transmitting a signal, comprising:
~~combining a direct sequence spread spectrum (DS-SS) signal with a frequency hopped spread spectrum (FH-SS) signal to provide a spreading signal;~~
~~spreading a digital signal with a the spreading signal code in a first domain;~~
~~converting the digital spread signal from the first domain to an analog signal a second domain; and~~
~~modulating the analog signal to produce an upconverted analog signal;~~
~~despread the upconverted analog signal in the second domain to provide a despread signal; and~~
transmitting the despread signal.

20-24. (Cancelled).

25. (Original) The method of claim 19, further comprising clipping the signal to reduce peaks associated with the signal.

26. (Currently Amended): A communication device comprising:
means for generating a direct sequence spread spectrum (DS-SS) signal spreading code;

means for combining the DS-SS signal spreading code with an input signal to produce a spread input signal;

means for clipping the spread input signal to remove peaks;

means for converting the spread input signal from a first domain to a second domain; and
means for dispersing the spread input signal in the second domain.

27. (Cancelled).